	Code No.: 15404	0
V	ASAVI COLLEGE OF ENGINEERING (Autonomous), HYDERABAD B.E. (ECE) III Year I-Semester Old Examinations, May/June-2019	
	Microprocessors and Microcontrollers	
Γim	ee: 3 hours Note: Answer ALL questions in Part-A and any FIVE from Part-B Max. Marks: 70	
	$Part-A (10 \times 2 = 20 Marks)$	
1.	Differentiate between minimum and maximum mode of operations of 8086μp.	
2.	Write the importance of Instruction Byte Queue of 8086μp.	
3.	Give the operation of following 8086 instructions with an example	
	i. DAS ii. PUSH	
4.	List any one string processing instruction of 8086µp with an example.	
5.	Neatly sketch the block diagram of 8253 Timer?	
6.	What is 8257? Which register(s) of it must be configured to interface with 8086µp?	
7.	Draw the PSW format of the 8051µc. And indicate each bit's significance.	
8.	List the built-in memories and I/Os available for 8051µc.	
9.	Name the interrupt sources of 8051 µc in the order of its priority.	
10.	Which IC is used to drive a stepper motor to be controlled by 8051 µc? Justify its need.	
	Part-B $(5 \times 10 = 50 \text{ Marks})$	
11.	 a) With a neat sketch explain about memory segmentation in 8086μp and list the features and advantages of each segment. 	[6]
	b) List any four addressing modes of $8086\mu P$ with suitable assembly instruction examples.	[4]
12.	a) Assume that there exists a total of 60 data-items (decimal numbers) starting from 2200H memory onwards in 8086 based system. Write an assembly language program to fetch the data and indicate the even numbers count in AL & odd numbers count in AH registers.	[5]
	b) Define procedure? Write any two techniques of passing parameters to a Procedure with suitable examples in 8086?	[5]

13. a) Draw the block diagram for 8255 and explain its operation in detail.

wave with 50Hz on GPIO pin: P2.1. Use Timer-0 in mode-1.

the memories.

of Port3.

b) Design a memory interface with 8086µP to interconnect two 4kB chips of EPROM

14. a) Draw the pin diagram of 8051 microcontroller and indicate the alternate pin functions

b) Assume that XTAL=11.0592 MHz, write 8051 assembly program to generate a square

and two 4kB chips of SRAM. Assume suitable starting address range for differentiating

[5]

[5]

[4]

[6]

15.	With a circuit diagram, explain the sensor interface through ADC0804 to 8051μC. Mention the steps involved in converting the Analog data into its digital form.	[6]
	b) Write 8051 assembly program to generate triangular wave by interfacing with 8-bit DAC?	[4]
16.	a) Illustrate the register organisation of the $8086\mu P$ in detail. Draw the flag register format of 8086 .	[6]
	b) Write any two Branching instructions; and, any two bitwise instructions supported by 8086μP with suitable examples?	[4]
17	Answer any two of the following:	
	a) Draw the interfacing of 8279 with 8086 microprocessor for interconnecting I/O.	[5]
	b) Describe RAM and ROM Memory Organisation of 8051 microcontroller.	[5]
	 c) Explain the design modules for interfacing 2x16 monochrome LCD with 8051μc. 	[5]

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